CIRCUIT FOR COMBINING AKB AND SELECTIVE BEAM CURRENT LIMITING AND PROJECTION TELEVISION SYSTEM UTILIZING SAME

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Related Application

This application claims the benefit of U.S. Provisional Application No. 60/275,722, filed March 14, 2001, Attorney Docket US010067P.

Background of the Invention

The invention relates to projection television systems including three separate cathode ray tubes (CRTs), and more particularly, to circuitry for combining automatic kinescope bias (AKB) circuitry with circuitry for sensing and limiting average individual beam current sensing.

Many CRT-based video display systems employ an automatic kinescope bias (AKB) control systems for maintaining proper black image current levels for each electron gun of an associated image displaying kinescope or CRT. The purpose of such AKB control systems in a CRT is to prevent the displayed picture colors and picture grey scale information from being adversely affected by variations in the kinescope bias due to such factors as aging, temperature changes, etc. Conventional AKB control systems include

a beam sampling element coupled to a control amplifier in each AKB feedback loop for automatically adjusting the kinescope black level of the electron gun. Adjustment is implemented typically during the blanking period to prevent variability of the black level sampling pulses.

A conventional AKB control system used in a video display system is disclosed in US Patent No. 4,633,321 ("the '321 patent). The '321 patent discloses an apparatus for automatically controlling the bias of an image display device such as a kinescope in a television receiver. The '321 patent apparatus comprises a grid system responsive to a grid drive signal in order to prevent visible artifacts during non-blanking AKB operating periods. More particularly, the kinescope electron gun is caused to conduct a white-going current during plural horizontal line intervals in response to the grid drive signal applied to the kinescope electron gun during a given portion of the AKB bias monitoring interval. The drive signal is blanked during horizontal retrace intervals to eliminate the white current at such times, thereby eliminating visible horizontal line retrace artifacts which would otherwise appear to the uncorrected retrace pattern of planar kinescope.

US Patent No. 5,488,417 ("the '417 patent") discloses an AKB system which avoids the need for grid drive circuitry to prevent visible artifacts. The '417 patent uses a controllable current source in an AKB circuit to apply a measurement current to a

kinescope driver amplifier during selected lines of the vertical interval of a video input signal. The amplified current signal induces a beam current in a kinescope coupled to the driver amplifier. A comparison circuit compares samples of the beam current obtained during the selected lines with a reference signal and applies a correction current to the driver amplifier for regulating a parameter, e.g., black level, of displayed images based on the comparison.

A signal source, coupled to the controllable current source, inhibits production of the measurement current during retrace portions of the selected lines and enables production of the measurement current during trace portions of the selected lines. The benefit of such a construction is that use of the signal source provides for suppression of visible artifacts due to AKB operation.

US Patent No. 6,188,435 B1 discloses a circuit for controlling beam current using current "pictures" for the R, G, B beam currents in the kinescope or color picture tube. The means for accomplishing the beam current control monitors each individual R, G and B beam current individually to maintain its corresponding picture sharpness and/or peak white maximum beam current, average beam current values per line and per picture. The current pictures correspond to the real currents in the color picture tube, the sum of which is compared with beam current information obtained from the associated high voltage transformer to both correct

fluctuations in the millisecond range and long-term drifts. Use of the current pictures enables the beam current control to quickly regulate picture sharpness for the three R, G, B colors as well as total beam current in the picture tube.

In order to generate the current pictures, the beam current control circuit taps or clamps the R, G, B signals in an amplifier including a controllable gain, amplified and weighted by means for gamma correction. The real current ratios in the picture tube are simulated in the current control circuit by the gamma correction means. A weighted sum is formed of the signals and serves as a comparison value with respect to a beam current information value, Icrt. The two information signals are compared, and an output signal is generated by the comparison. The output signal is used to control the gain of the amplifier. The current control circuit thereby monitors the values of each individual R, G, B signals to regulate the R, G, B beam currents and picture sharpness, and the weighted sum together with the output signal of the control circuit are monitored by a decision circuit.

In projection television display systems having three separate CRTs for producing R, G, B light, certain signals such as a high level flat field in an individual primary color can result in the respective CRT providing much more than its nominal share of light. While it is common practice to sense and control the total beam current, under such flat field conditions, one CRT may draw most if

not all the beam current ordinarily allocated for all three CRTs. When this occurs, overheating and fracturing of the CRT may occur. This is particularly troublesome in the case of video accessory devices which provide a blue flat field when no program content is being provided.

To accommodate these undesirable signal conditions, it would be effective to determine the relative share of current provided to each CRT. In the prior art, for example, in a Philips/Magnavox GR-9D, the cathode current of the blue CRT is directly measured, with the preceding R, G, B video gains controlled to limit the average blue current to a safe level. The skilled artisan, however, will realize that it is also desirable to utilize individually sampled cathode currents for automatic CRT cut-off stabilization, that is, automatic kinescope bias (AKB) control.

<u>Objects and Summary of the Present Invention</u>

Accordingly, it would be a welcome advancement for those skilled in the art to realize a circuit and method which allows the sampled current to simultaneously supply both the AKB control circuitry and blue drive limiting circuitry, with no interaction therebetween.

It is therefore an object of the present invention to provide a control circuit which monitors the average blue beam current, and determines if that blue beam current exceeds a predetermined

threshold, introduces a gain reduction in preceding video gain stages to limit the blue beam current, and which, depending on the determined magnitude of the blue beam current, may deploy either AKB or selective beam limiting without the use of special timing signals.

To that end, the present invention discloses a control circuit for use in a video processor which utilizes combined automatic kinescope bias (AKB) control, and average individual beam current sensing and limiting in at least one CRT. The control circuit includes automatic kinescope bias (AKB) control circuitry for detecting a magnitude of individual red (R), green (G) and blue (B) cathode currents driving corresponding R, G and B CRTs, generating at least one of R, G and B average cathode current control signals therefrom, and using at least one of the R, G and B average cathode current control signals as a feedback signal to the video processor to reduce the R, G and B cathode currents approximately equal current amounts. Selective beam current limiting circuitry within the control circuitry compares at least one of the R, G and B average current control signals with a predetermined signal, and whereupon the at least one of the R, G and B average current control signals exceeds the predetermined signal, introducing a gain reduction in corresponding video gain stages within the video processor to limit the at least one of the R, G and B average current control signals.

Brief Description of the Drawing Figures

The above and other more detailed aspects of the invention will be described in detail hereinafter, by way of example, with reference to the following drawing figures.

Fig. 1 is a schematic diagram of a conventional Automatic Kinescope bias (AKB) control circuit;

Fig. 2 is a schematic diagram of one embodiment of a circuit of this invention comprising both AKB circuitry and average beam current sensing and limiting circuitry; and

Fig. 3 is a schematic circuit diagram of a proprietary video board which implements the function of the present invention.

Detailed Description of the Preferred Embodiments

The reader should note that the embodiments described herein are for exemplary purposes only, and are not meant to limit the scope and spirit of the invention at all. Only the language of the claims appended hereto shall limit the scope and spirit of the invention.

Prior art Fig. 1 shows a video processor with conventional and automatic kinescope bias control. More particularly, the video processor generates and outputs a red (R), green (G) and Blue (B) cathode current signals. The blue cathode current signal is supplied to NPN emitter follower Q4, the collector current of which

drives PNP emitter follower Q5. The red cathode current signal is supplied to NPN emitter follower Q9, the collector current of which drives PNP emitter follower Q10. The green cathode current signal is supplied to NPN emitter follower Q14, the collector current of which drives PNP emitter follower Q15. Emitter currents of Q5, Q10 and Q15 drive CRTB, CRTG and CRTR, respectively.

Collector currents of Q5, Q10 and Q15 are fed back to the video process which, using DC bias control, maintains equal near-black currents, i.e., the blue, green and red video processor outputs shown. The present invention exploits the presence of one at least one of the three currents flowing in through the collectors of respective PNP transistors Q5, Q10 and Q15 to additionally detect average CRT cathode beam currents and determine if they exceed a specific (appropriate) current level. If the particular CRT beam current exceeds the specified level, video drive circuitry is provided with a control signal to control and limit the current level.

Figure 2 shows a first embodiment of a control circuit of this invention. The Fig. 2 circuit adds average beam current sensing and limiting circuitry to the conventional AKB circuitry shown in Fig. 1 and described above. Only the additional circuitry for one of the driver currents, that is, the blue current is shown and described in order to simplify the drawing and explanation.

The collector current passing through PNP transistor Q5 is

approximately equal to the transistor's emitter current driving the blue CRT. The cathode current is therefore returned through resistor R1, PNP transistor Q1 and diode D1 to the AKB feedback port of video processor V2. The voltage drop across R1 is proportional to the average blue beam current. When the voltage drop across R1 exceeds a predetermined threshold current determined by the voltage across resistor R2, PNP emitter follower Q2 and NPN emitter follower Q3 conduct and introduce gain reduction via video gain circuitry internal to video processor V2. Resistor R5 is connected in parallel with a capacitor C2 to provide low pass filtering means to filter the signal driving NPN transistor Q3. The currents output from the blue green and red ports of the video processor are limited thereby. So, depending on the magnitude of the current, either AKB or selective beam limiting is deployed by the inventive circuit without a need for additional special timing signals.

The above-described circuit may be used successfully in a projection television display system with three separate CRTs for producing red, green and blue light (signals).

Fig. 3 is a schematic diagram showing a production design of a video processing circuit which implements the concept of this invention, that is, utilizes sampled CRT cathode beam current for both automatic cut-off stabilization and drive limiting circuitry without interaction. One portion of the video processor circuit,

portion A shown enclosed by the broken line of Fig. 3, implements the control circuit of the invention.

Portion A performs an equivalent function of the inventive circuitry shown in Fig. 2. The reader should note that portion A only highlights the blue drive portion of the circuitry, as is the case of Fig. 2.

In portion A, PNP transistors 7614 and 7615, and NPN transistor 7616 correspond to PNP transistors Q1 and Q2, and NPN transistor Q3 as shown in Fig. 2. Resistor 3665 and capacitor 2625 correspond to resistor R5 and capacitor C2, resistors 3663, 3666 and 3664 correspond to resistors R2, R3 and R4, resistor 3669 is used in lieu of diode D1, and resistors 3667 and 3668 are included in Fig 3, but not Fig 2, and low pass filter combination R1 and C1 of Fig. 2 is not included in Fig. 3. The current flowing at node F616 is equivalent to the current flowing in the collector of PNP transistor Q5.